

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L2	34	(microcontroller same hardware same configuration) and ((program\$4 adj microcontroller) or (configur\$3 adj microcontroller))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/12/08 10:21
L3	1	markup and 2	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/12/08 10:21
L4	18967	xml	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/12/08 10:22
L5	943	microcontroller same hardware same configuration	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/12/08 10:22
L6	13	4 and 5	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/12/08 10:46
L7	587	fpga and xml	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/12/08 10:46
L8	9	fpga same xml	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/12/08 10:51
L9	46520	microcontroller	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/12/08 10:52
L10	434	4 and 9	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/12/08 10:52
L11	67	fpga and 10	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/12/08 10:52

S1	866	microcontroller same hardware same configuration	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/12/08 10:22
S2	7	microcontroller same hardware same (configuration adj information)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/09/24 10:17
S3	2115	(program\$4 adj microcontroller) or (configur\$3 adj microcontroller)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/09/24 10:19
S4	31	(microcontroller same hardware same configuration) and ((program\$4 adj microcontroller) or (configur\$3 adj microcontroller))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/12/08 10:21